

## IN THE SPECIFICATION

Please replace paragraph [40] with the following paragraph:

For every ultrasound beam, the cache memory and controller 112 connects over digital signal lines ~~132-133~~ (e.g., carried by a separate flex cable) to each signal processor controller which may be included within memory 108 on each processing board 106. The signal processor controller which may be included within memory 108 are drawn as a separate block labeled 'memory' on the processing board 106, but may also be included as part of the signal processor 110. The cache memory and controller 112 transfers static and dynamic probe setup information to the signal processor 110. Static setup information is typically spatial element locations, power settings, and delay setting mapping tables. Dynamic information is typically directional information for the sub-apertures that vary from beam to beam. The digital signal lines may include, for example, a clock line for each processing board 106, a serial command data line for each processing board 106, one or more data lines connected to each processing board 106, an output enable for one or more of the signal processors 110, and a test signal.

Please replace paragraph [97] with the following paragraph:

[0097] The circuitry in the signal processor 110 is described in more detail below with regard to Figure 10. Figure 10 shows the narrowband beamforming circuitry 1000 in the signal processor 110. Each receive input (one of which is labeled  $s_{xIn0}$ ) passes through a low-noise amplifier 1002, a weighting and summation stage including mixers (one of which is labeled 1004), summers (a positive summation summer labeled 1005 and a negative summation summer labeled 1006), and all-pass filters 1008 and 1009. ~~in~~ In addition the all-pass filters connect to second

summers (one of which is labeled 1010) and through a line driver 1012 out to the receive sub-aperture output (one of which is labeled sxOut).

Please replace paragraph [102] with the following paragraph:

[00102] The summation stage 1010, having the second summers, may further include an attenuation to level the signal swing of the available range.

#### AMENDMENTS TO THE DRAWINGS

The attached sheets of drawings include changes to Figure 1. This sheet replaces the original sheet for Figure 1. In Figure 1, reference numeral 132 has been replaced with reference numeral 133.